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42717 7590 07/12/2005

HAYNES AND BOONE, LLP
 901 MAIN STREET, SUITE 3100
 DALLAS, TX 75202

08/23/2005 HDESTA2 00000113 10600393

01 FC:1501 1400.00 OP
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I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

Kristina Garcia (Depositor's name)
Kristina Garcia (Signature)
August 18, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/600,393	06/20/2003	Chi-Chun Chen	2002-0066 / 24061.461	8529

TITLE OF INVENTION: METHOD OF FORMING DUAL GATE INSULATOR LAYERS FOR CMOS APPLICATIONS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	10/12/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
THOMAS, TONIAE M	2822	438-199000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Haynes and Boone, LLP

2 _____

3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Taiwan Semiconductor Manufacturing Company, Ltd.**Hsin-Chu, Taiwan, R.O.C.**

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

☒ Issue Fee☒ Publication Fee (No small entity discount permitted)☐ Advance Order - # of Copies _____

4b. Payment of Fee(s):

☒ A check in the amount of the fee(s) is enclosed.☐ Payment by credit card. Form PTO-2038 is attached.

☒ The Director is hereby authorized to charge the required fee(s), or credit any overpayment, to Deposit Account Number 08-1394 (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

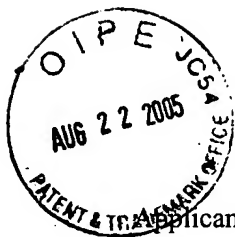
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Authorized Signature _____

Date 8-18-05Typed or printed name David M. O'DellRegistration No. 42,044

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Chi-Chun Chen, et al.	§	Docket No.:	2002-0066 / 24061.461
Serial No.:	10/600,393	§	Examiner:	Toniae M. Thomas
Filed:	June 20, 2003	§	Art Unit:	2822
For:	Method for Forming Dual Gate Insulator Layers for CMOS Applications	§	Conf. No.:	8529
		§		
		§		

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Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL

Dear Sir:

In response to the Notice of Allowance and Issue Fee Due mailed on July 12, 2005, enclosed are the following:

1. Part B – Fee(s) Transmittal (in duplicate);
2. Check in the amount of \$1,700.00; and
3. Return postcard.

The Commissioner is hereby authorized to charge Deposit Account No. 08-1394 for any deficiencies in the enclosed fees. This sheet submitted in duplicate.

Respectfully submitted,

David M. O'Dell
Registration No. 42,044

Date: 8-18-05

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Dallas, Texas 75202-3789
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Facsimile: 214-200-0853

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on the date below:

Kyphina A. Pavia
Name

August 18, 2005
Date